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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/735,402

Applicants: William Lovett, et al.

Filed: December 12, 2003

Docket No.: 45256.00051

Title: METHOD AND APPARATUS FOR  
PERFORMING LAZY BYTESWAPPING  
OPTIMIZATIONS DURING PROGRAM CODE  
CONVERSION

Customer No.: 36183

Confirmation No.: 2114

TC/A.U.: 2193

Examiner: Chaki, Kakali

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)(3)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the Applicant's duty under 37 CFR § 1.56, the following information is brought to the attention of the Examiner. The items are listed on the attached form PTO/SB/08A, and PTO/SB/08B.

The items identified in this Information Disclosure Statement (IDS) may or may not be "material" pursuant to 37 CFR § 1.56 and the submission thereof by Applicant shall not be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR § 1.97(h)), or even qualifies as "prior art" under 35 USC § 102 with respect to this invention unless specifically designated by Applicant as such.

The filing of this IDS shall not be construed to mean that a search has been made or that no other material information, as defined in 37 CFR § 1.56, exists.

The attached IDS is being filed in accordance with 37 CFR §§ 1.97 and 1.98. This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b)(3), that is before the mailing of a first Office Action on the merits. Thus, no petition or fee is required. However, if the undersigned representative of Applicant is in error in this regard, then the Examiner is requested to consider this IDS as filed under § 1.97(c) and is further authorized to charge any fee required by the filing of these papers to Deposit Account No. 50-2613.

Respectfully submitted,

Dated: July 13, 2005


By: 

Bradley D. Blanche  
Reg. No. 38,387  
Attorney for Applicant

**CERTIFICATE OF MAILING**

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*I hereby certify that this paper(along with anything referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.*

  
Angela Williams

<sup>14 AW</sup>  
July 13, 2005  
Date



PTO/SB/08A (08-03)

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Application Number	10/735,402
Filing Date	December 12, 2003
First Named Inventor	William Lovett
Art Unit	2193
Examiner Name	Chaki, Kakali
Attorney Docket Number	45256.00051.UTL

[illegible][illegible]Date  
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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1

of

1

**Complete if Known**

Application Number	10/735,402
Filing Date	12/12/2003
First Named Inventor	William Lovett
Art Unit	2193
Examiner Name	Chaki, Kakali
Attorney Docket Number	45256.00051.UTL

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		MATHEW J. HOSTETTER, CLIFFORD T. MATTHEWS: "Executor Internals: How to Efficiently Run Mac Programs on PCs" 'Online! 1996, XP002300303 Retrieved from the Internet: URL: <a href="http://www.ardi.com/MacHack_96.html">http://www.ardi.com/MacHack_96.html</a>	
		Program of MacHack '96 (confirming the publication date of document D1)" 'Online! XP002300340 Retrieved from the Internet: URL: <a href="http://www.machack.com/96/Sessions.html">www.machack.com/96/Sessions.html</a>	
		AVINASH C. PALANISWAMY, et al.: "An Efficient Implementation of Lazy Reevaluation." 25th Annual Simulation Symposium (Cat. No. 92TH0433-3) IEEE Comput. Soc. Press Los Alamitos, CA, USA, 1992	
		DAVID UNG AND CRISTINA CIFUENTES: "Machine-Adaptable Dynamic Binary Translation" Proceedings of the ACM SIGPLAN Workshop on Dynamic and Adaptive Compilation and Optimization, January 2000	
		CRISTINA CIFUENTES, et al.: "The Design of a Resourceable and Retargetable Binary Translator" Reverse Engineering, 1999. Proceedings, Sixth Working Conference on Atlanta, GA, USA 6-8 Oct. 1999, Los Alamitos, CA, USA, IEEE Comput. Soc, US, 6 October 1999	

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